



08/22/2003 15:38 17037266024

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Takashi KOBAYASHI et al.

Serial No.: 10/020,573

Group Art Unit: 2826

Filed: October 31, 2001

Examiner: Greene, P.

For: VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE

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